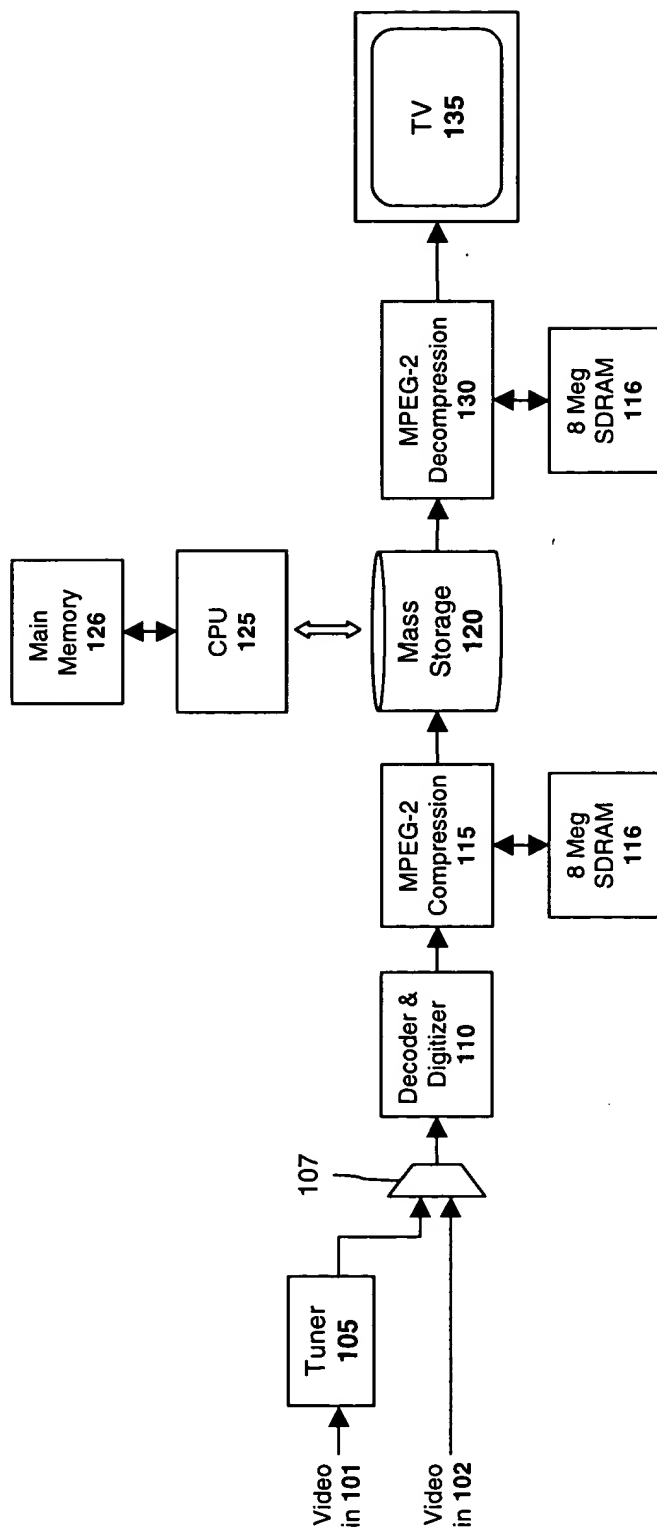
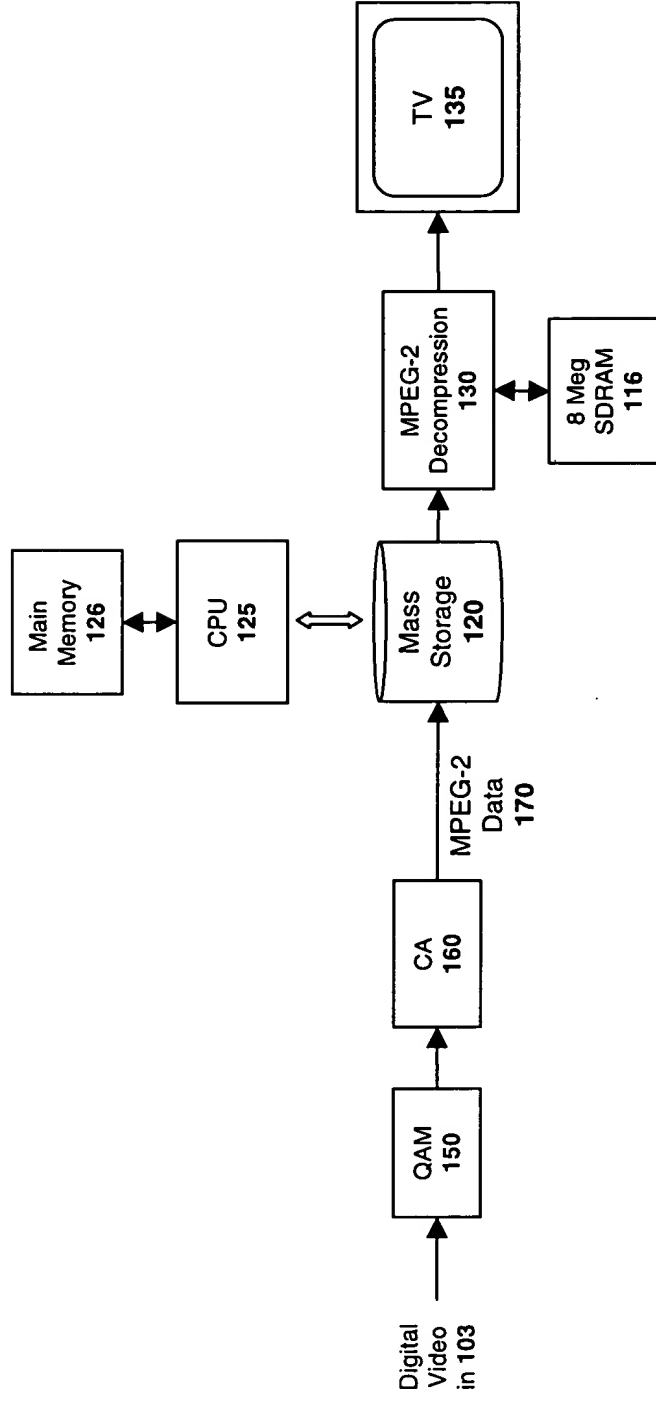


FIG. 1a is a block diagram of a system architecture.



**FIG. 1a**  
*(prior art)*

FIG. 1a



**FIG. 1b**  
(prior art)

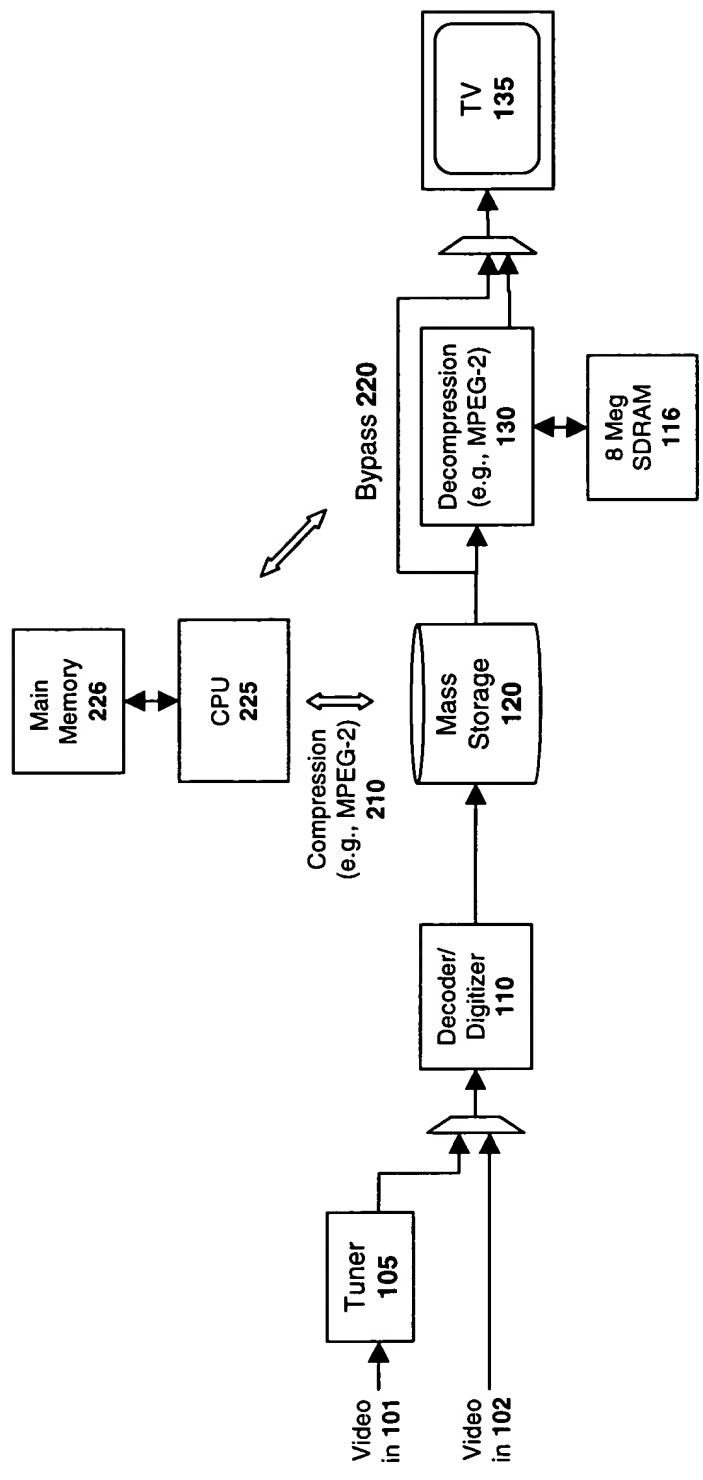


FIG. 2

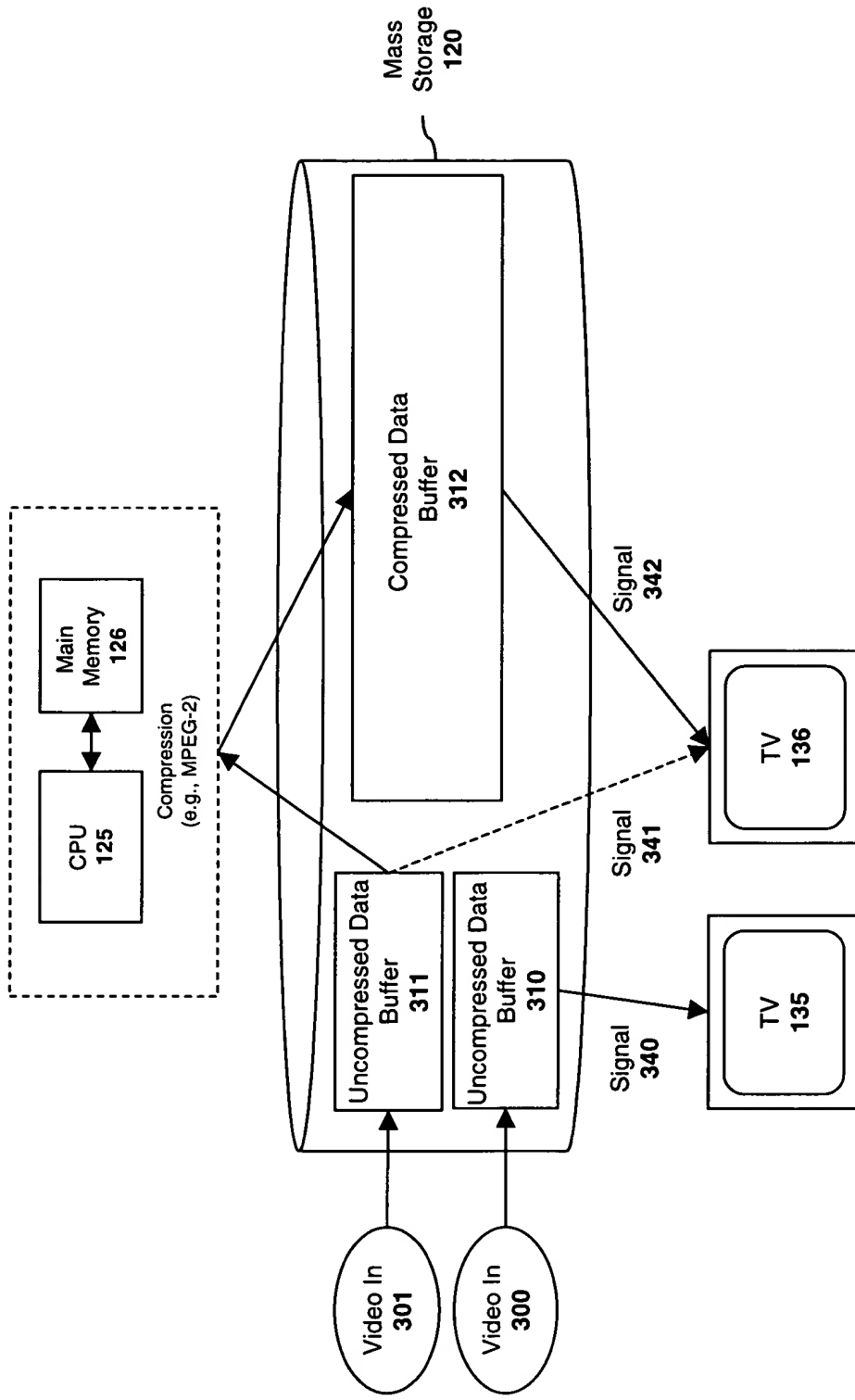
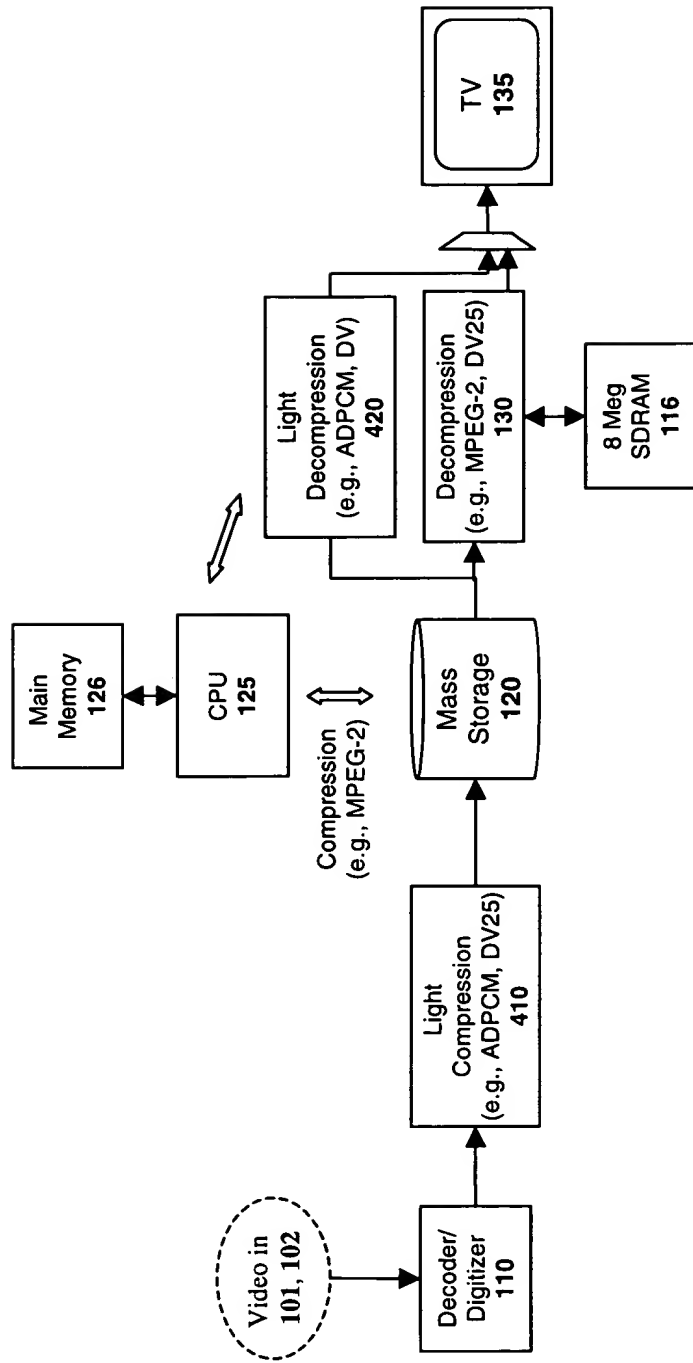
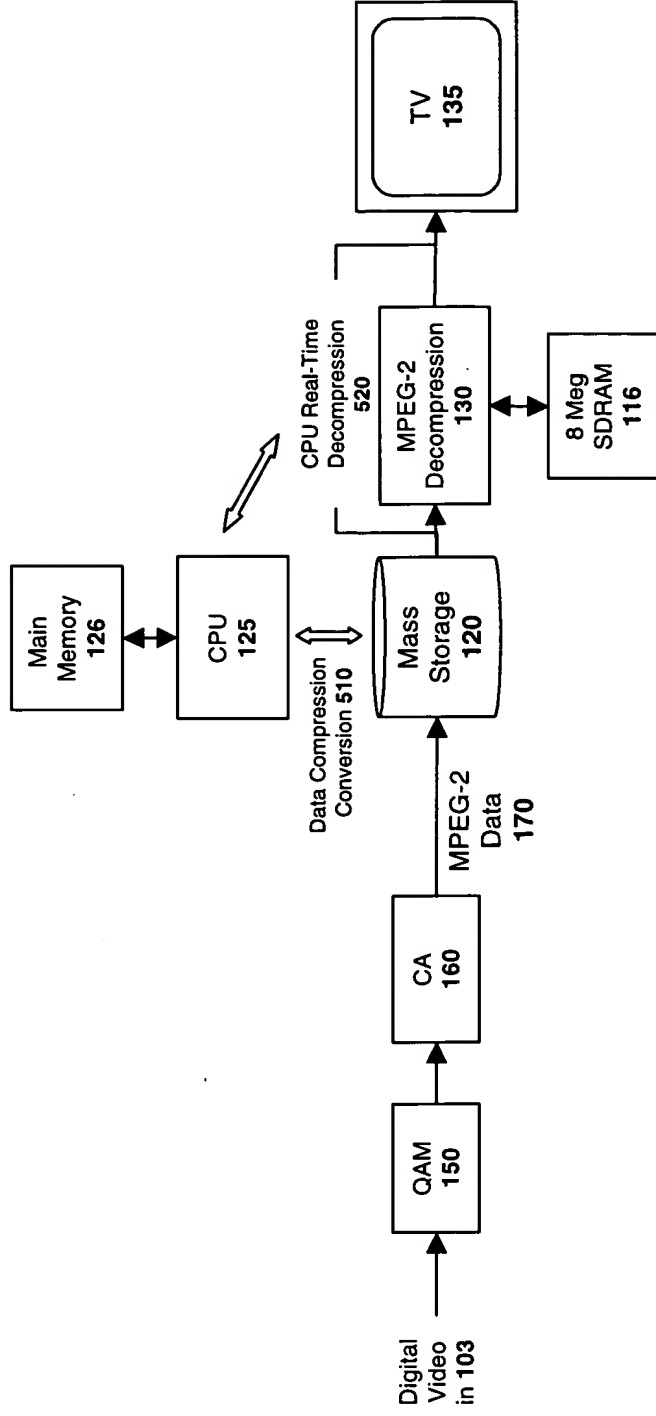


FIG. 3



**FIG. 4**



**FIG. 5**

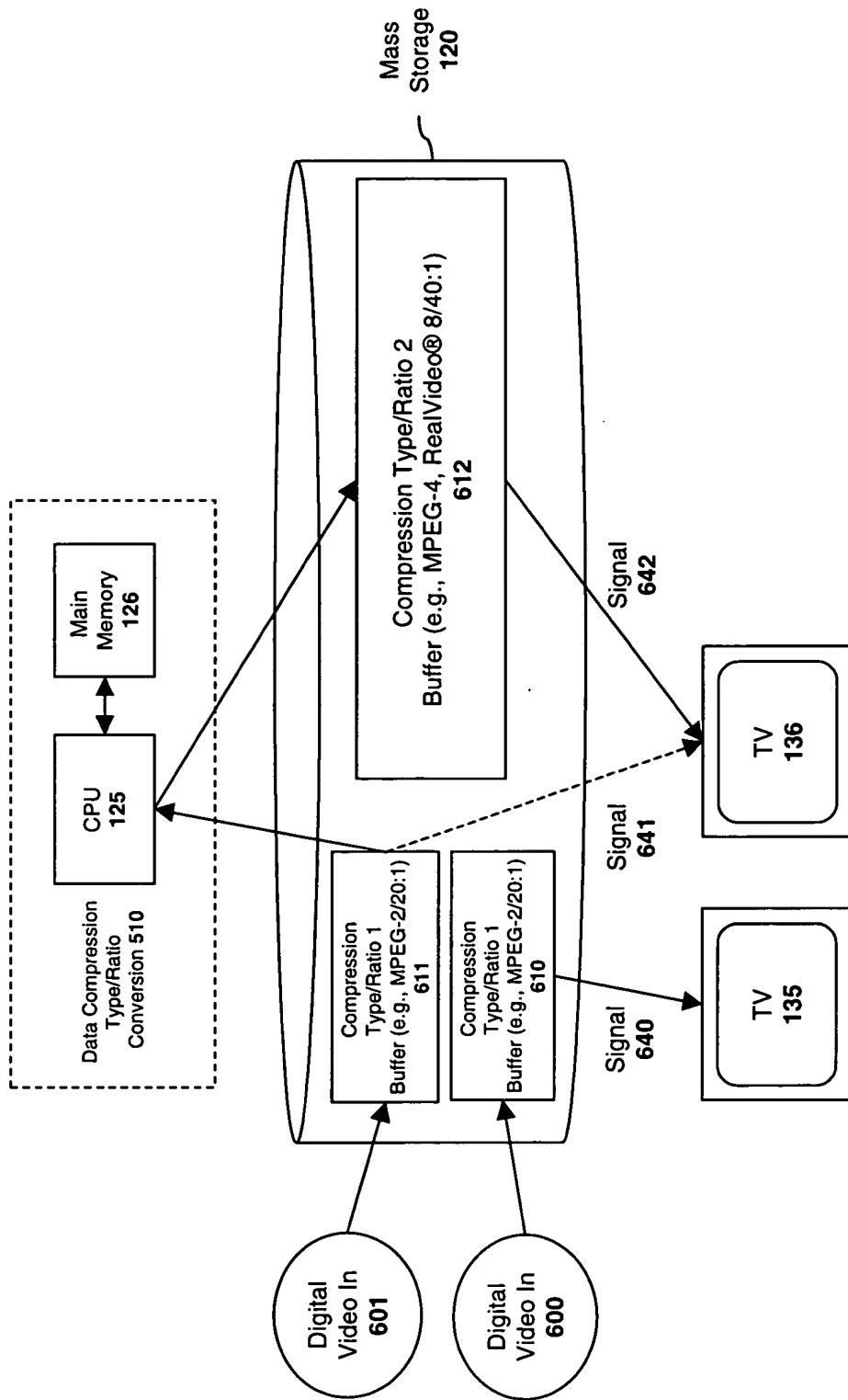


FIG. 6

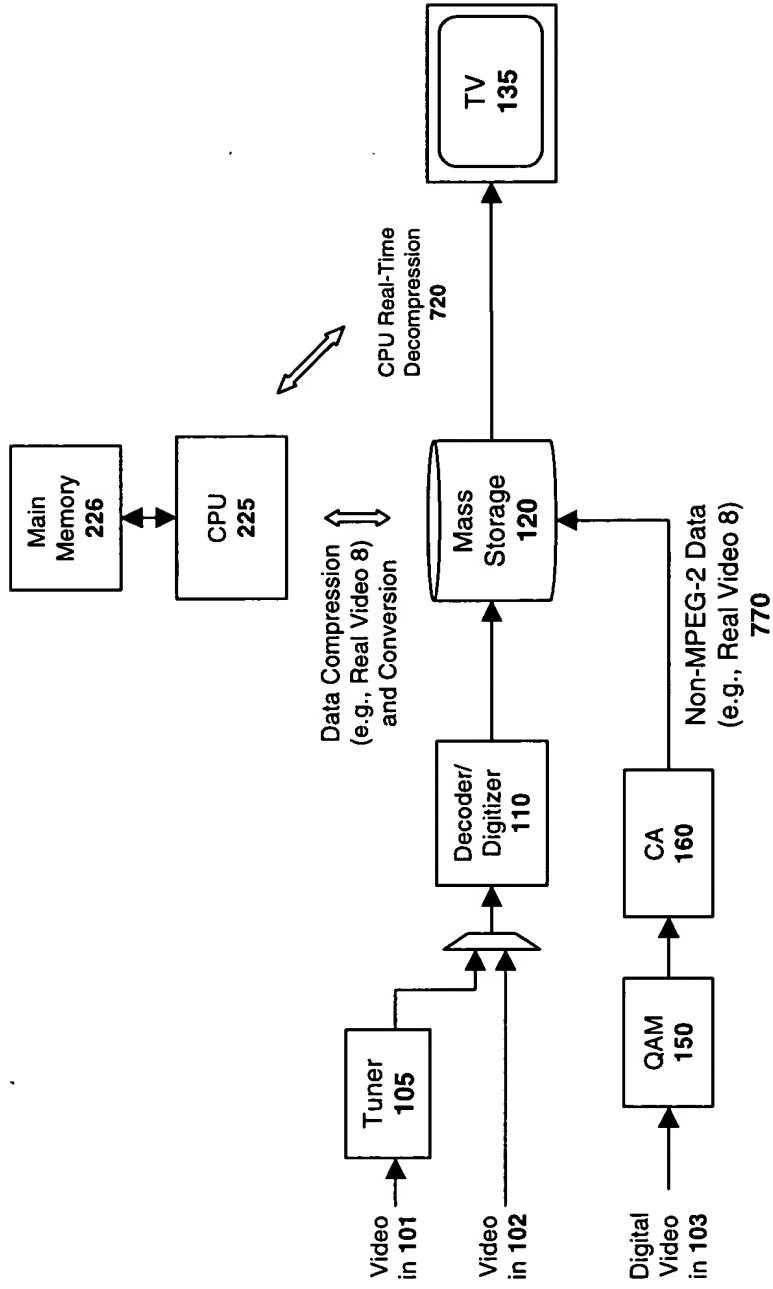


FIG. 7a



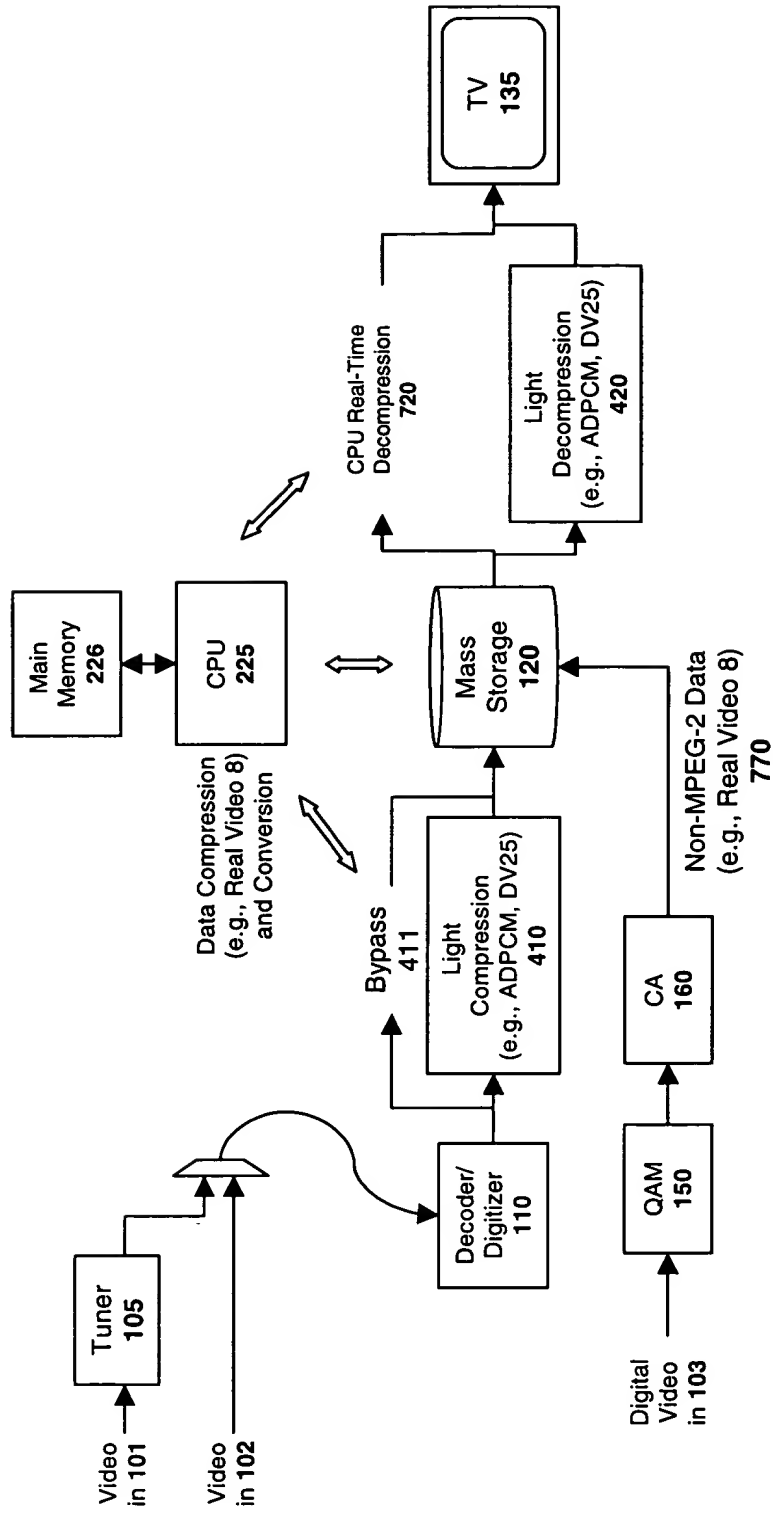


FIG. 7b

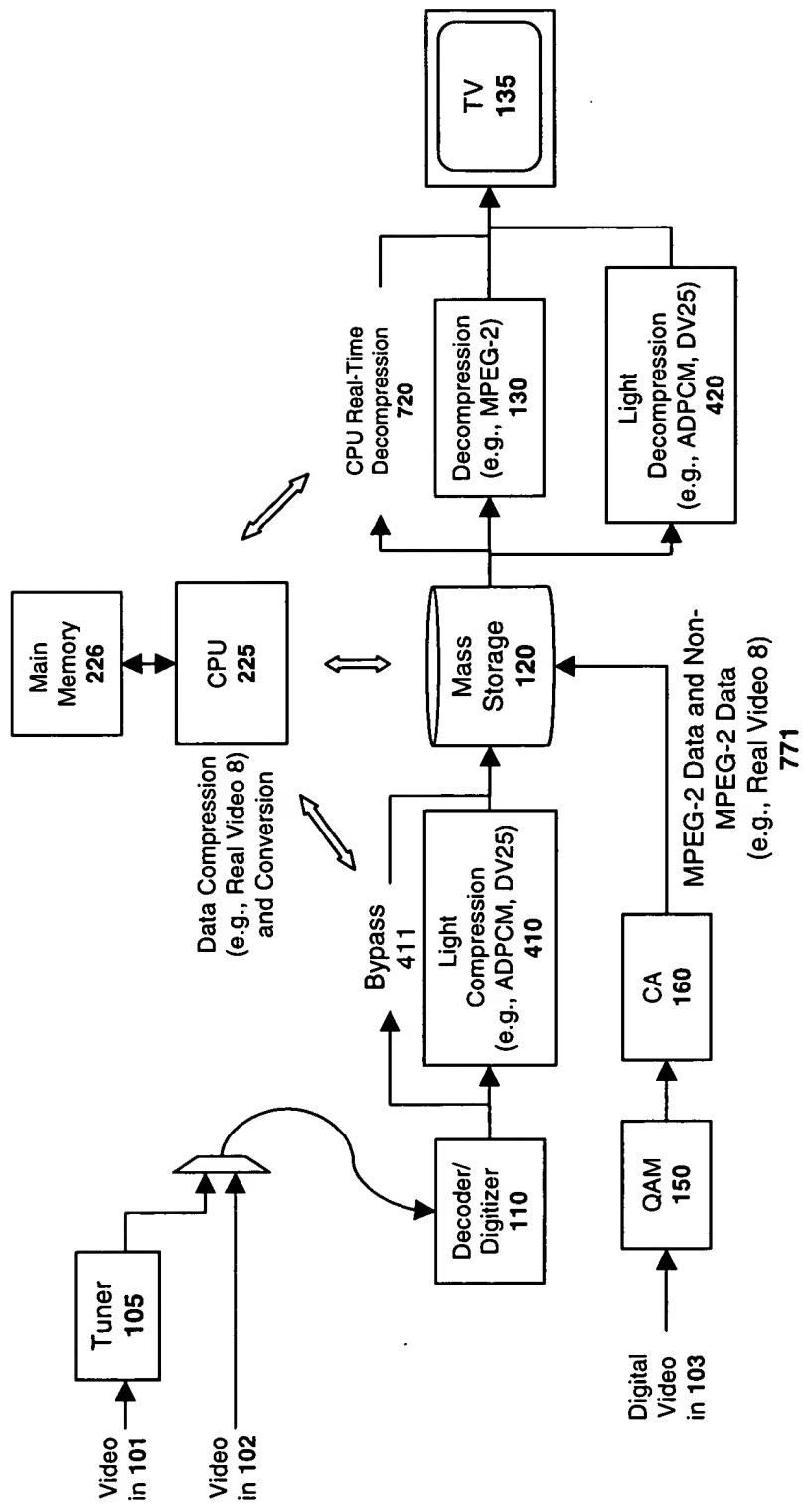


FIG. 7c